Docket No.: 51441-016

REQUEST FORM FOR CONTINUATION APPLICATION UNDER 35 USC 111(a)

Prior Application: **International Application No. PCT/JP99/01034**
Art Unit:
Examiner:

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

This is a Request for filing a continuation application, entitled LCD DISPLAY DEVICE WITH DISPLAY DENSITY ADJUSTING FUNCTION, under 35 U.S.C. 111(a) of pending prior International Application No. PCT/JP99/01034, filed on March 4, 1999, entitled LCD DISPLAY DEVICE WITH DISPLAY DENSITY ADJUSTING FUNCTION, which claims priority from Japanese Patent Application Serial No. 75098/1998 filed on March 10, 1998, by the following named inventor:

INVENTOR	RESIDENCE	CITIZENSHIP	POST OFFICE ADDRESS
Koji OGUMA	Tokyo, Japan	Japanese	c/o Tainita Corporation, 14-2, Maeno-cho 1-Chome, Itabashi-ku, Tokyo, 1740063 JAPAN

1. ☐ I hereby verify, to the best of my knowledge, that the enclosed copy of this prior application is a true copy of the above-identified prior application, including the oath or declaration as originally filed.

2. ☐ Preliminary Amendment is enclosed.

2a. ☒ An Information Disclosure Statement and PTO1449 Form are submitted herewith.

3. ☐ Cancel claims .

4. The filing fee is calculated on the basis of the claims existing in the prior application as amended at 3 above:

	NO. OF CLAIMS		EXTRA CLAIMS	RATE	AMOUNT
Total Claims	1	-20	0	$18.00 =	$0.00
Independent Claims	1	-3	0	$78.00 =	$0.00
				Basic Application Fee	$760.00
				If multiple dependent claims are presented, add $260.00	$0.00
				Total Application Fee	$760.00
				Subtract ½ if small entity	$0.00
				TOTAL APPLICATION FEE DUE	$760.00
				AMOUNT TO BE CHARGED TO DEPOSIT ACCOUNT NO. 500417	**$760.00**

4a. ☐ Enclosed is a Verified Statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.

4b. ☐ A verified Statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27 was filed in prior application and such status is still proper and desired.

5. ☐ The Commissioner is hereby authorized to charge fees under 37 CFR 1.16 and 1.17 which may be required, including any extension of time fees including any extension of time fees to maintain the pendency of the parent application Serial No. ___ or credit any overpayment to Deposit Account No. 500417.

6. ☒ Amend the specification by inserting before the first line the sentence:
--This application is a Continuation of International Application No. PCT/JP99/01034, filed March 4, 1999, which is incorporated herein by reference.--

7. ☒ Priority of Patent Application Serial No. 75098/1998 filed on March 10, 1998, in Japan and International Application No. PCT/JP99/01034 filed March 4, 1999 designating the U.S. is claimed under 35 USC 119, 120 and/or 365. A copy of the published International Application is enclosed.

8. ☒ The prior application is assigned of record to Tanita Corporation

9. ☒ The power of attorney in the prior application is to: Nakamura & Associates

10. ☒ Also enclosed:
1. Form PCT/IB/304
2. Form PCT/IB/301
3. Cover Page of Published International Application
4. International Search Report

11. ☐ A petition, fee and response has been filed to extend the term in the pending prior application until .

12. ☒ Address all future communications to: (May only be completed by applicant, or attorney or agent of record)

McDERMOTT, WILL & EMERY
600 13th Street, N.W.
Washington, DC 20005-3096

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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SPECIFICATION

LCD DISPLAY DEVICE WITH DISPLAY DENSITY ADJUSTING FUNCTION

FIELD OF THE INVENTION

The present invention relates to a LCD display device whose display density is preferably adjustable.

BACKGROUND OF THE INVENTION

Since liquid crystal display element (hereafter, referred to as LCD) consumes rather small amount of current, and a large number of display pixels (hereafter, referred to as display segments) can be driven by a driving system called dynamic drive with a small number of terminals, said LCD is effective on down sizing and thereby has been used frequently in various fields.

When a large amount of display segment is driven by a small number of terminals, however, LCD shows some drawbacks as:

① a visible angle for clear display is limited (see Fig. 1);

② when the drive voltage varies, density of display is made to vary also, which makes visibility rather difficult; and

③ depending on the temperature, density of display varies, which also makes visibility rather difficult.

If a large amount of terminals could be used to drive a large amount of display segments, these problems would not occur, but as a matter of fact it is rather difficult to make such packaging.

Though, in general, as countermeasures to solve these problems, a stabilized voltage is applied to a LCD drive unit to prevent variation of the drive voltage, and, on the other hand, when the density of display changes due to the variation of the visual angle or the temperature, the drive voltage is adjusted using volume knob so as to provide good visibility at that time, it is rather troublesome and inconvenient for a user to have to adjust every time when the using environment changes, and at the same time the number of components increases resulting in cost-increase (see Figs. 2, 3, 4).

The object of the present invention is to provide LCD display device capable of always supplying fine LCD display suitable for its using condition without depending on a means for adjusting LCD drive voltage by volume knob and without significant cost increase.

DISCLOSURE OF THE INVENTION

According to a characteristic feature of the present invention, a using condition is detected and a time period (T_0) for adjusting a voltage difference between all common terminals and all segment terminals connected to the LCD to be equal or near to zero is inserted into one frame period of LCD drive so that the effective voltages of common terminals and segment terminals may be controlled to be voltages suitable for detected using condition.

According to an embodiment of the invention, a mechanism for detecting the using condition is provided such as: a voltage

detector is added when the using voltage varies due to the battery or the like; a temperature detector is added by the use of temperature sensitive resistance when the temperature varies; the using condition is detected by the key operation when the visual angle varies depending on the using condition; and the density of display of the LCD is adjusted based on a signal from an input device by providing a key for density of display so as for the user to operate said key when the using condition is not obvious, and thereby, based on the stored or input information about conditions, the time period (T_0) for adjusting a voltage difference between all common terminals and all segment terminals connected to the LCD to be 0 V is provided and said time period (T_0) is made to be variable so that the effective voltage may be changed.

There will now be described the present invention in detail based on a preferred embodiment thereof with reference to the attached drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is an explanatory drawing for illustrating visibility of the LCD display;

Fig. 2 is a block diagram illustrating an example of the conventional LCD drive circuit;

Fig. 3 shows some examples of drive voltage waveform of the conventional LCD;

Fig. 4 is a general correlation diagram of brightness and effective voltage;

Fig. 5 is a block diagram illustrating a LCD drive circuit of an embodiment of the present invention;

Fig. 6 shows LCD drive voltage waveforms of an embodiment of the present invention;

Fig. 7 also shows LCD drive voltage waveforms of an embodiment of the present invention; and

Fig. 8 is a correlation diagram of brightness and effective voltage of an embodiment of the present invention.

PREFERRED EMBODIMENT OF THE PRESENT INVENTION

Fig. 5 shows a block diagram of an embodiment of the invention, where a drive system is 1/2 bias 4 time division, T0 = M * T, and M is integer equal to or more than 0.

The difference of the present embodiment from the conventional 1/2 bias 4 time division is in only these point below:

① a counter counts, according to numerical value equal to or more than 0 given by a controller, from 0 to (3+M) repeatedly, and outputs the counted value; and

② when the numerical value given by the counter is equal to or more than 4, a selector for common terminal and a selector for segment terminal select and output V_Q for all output terminals;

which means that what is required is quite a simple addition.

The controller calculates an optimum density value M for the current condition by a voltage detector and a key switch (not shown) and outputs it to the counter.

The counter counts the number of times from 0 to (3+M) repeatedly every time when T is input.

The selector for common terminal selects and outputs V_Q, based on the output value from the counter, to COM1 if it is 0, to COM2 if it is 1, to COM3 if it is 2, or to COM 4 if it is 3. If it is equal to or more than 4, V_Q is output to all COM terminals. In other case, $V_G = 1/2\ V_{DD}$ is output.

RAM for display outputs to the selector for segment terminal, based on the output value from the counter, a n-bit data corresponding to COM1 if it is 0, that corresponding to COM2 if it is 1, that corresponding to COM3 if it is 2, or that corresponding to COM 4 if it is 3. If the value is equal to or more than 4, all of the n-bit data is output as 0.

The selector for segment terminal outputs to the corresponding segment terminals, based on the n-bit data output from the RAM for display, V_N if the bit is 1, and V_Q if the bit is 0.

When the LCD drive unit described in the block diagram (Fig. 5) is employed, the outputs shown in Figs. 6, 7 are obtained.

During the time period (T_0), the same voltage V_Q is applied to all terminals. In case of $T_0 = M * T$, the effective value of voltage $V_{4OFF}(M)$ applied to the unlighted segment (f) is calculated as:

$$V_{4OFF}(M)^2 = \{0^2T + 1/2V_{DD}^2 \times 3T/2 + (-1/2V_{DD})^2 \times 3T/2 + 0^2 \times MT\} / (4+M)T$$

$$= V_{DD}^2 \times 3 / (16 + 4 \cdot M)$$

$$V_{4OFF}(M) = (3 / (16 + 4 \cdot M))^{1/2} \times V_{DD}$$

The effective voltage $V_{4ON}(M)$ applied to the lighted segment (e) is calculated as:

$$V_{4ON}(M)^2 = \{(-V_{DD})^2 \times T/2 + V_{DD}^2 \times T/2 + (1/2V_{DD})^2 \times 3 \times T/2 + (-1/2V_{DD})^2 \times 3 \times T/2 + 0^2 \times M \times T\} / (4 + M)\,T$$

$$= V_{DD}^2 \times 7 / (16 + 4 \cdot M)$$

$$V_{4ON}(M) = (7 / (16 + 4 \cdot M))^{1/2} \times V_{DD}$$

In case of M = 0, comparing above expressions with those for the typical 1/2 bias 4 time division of the "conventional example", they coincide with each other as:

$$V_{4OFF}(0) = (3 / 16)^{1/2} \times V_{DD} = V_{4OFF}$$

$$V_{4ON}(0) = (7 / 16)^{1/2} \times V_{DD} = V_{4ON}$$

That is, when M = 0, the conditions is identical with those of conventional 1/2 bias 4 time division in which "the time period (T_0) which causes the voltage difference between all common terminals and all segment terminals connected to the LCD to be zero is not inserted".

Above expressions can be rewritten as follows:

$$V_{4OFF}(M) = (3 / (16 + 4*M))^{1/2} \times V_{DD} = (3 / 16)^{1/2} \times (4/(4+M))^{1/2} \times V_{DD} = (3 / 16)^{1/2} \times V_{DD}'$$

$$V_{4ON}(M) = (7 / (16 + 4*M))^{1/2} \times V_{DD} = (7 / 16)^{1/2} \times (4/(4+M))^{1/2} \times V_{DD} = (7 / 16)^{1/2} \times V_{DD}'$$

where, $V_{DD}' = (4/(4+M))^{1/2} \times V_{DD}$

That is, since an apparent drive voltage V_{DD}' may be changed by changing M without changing V_{DD}, the change of M provides the same effect as that caused by the change of V_{DD}. Therefore, it is proved that, according to the invention, LCD display may be held in optimum condition by adding quite a small logic portion

without adjusting V_{DD} by volume knob.

The case where the present invention is applied to an equipment in which V_{DD} varies will be described as an example. Above expressions may be rewritten as:

$$V_{DD}'^2 = V_{DD}^2 \times 4/(4+ M)$$

$$M = 4 \times V_{DD}^2 / V_{DD}'^2 - 4$$

In the equipment in which two pieces of single-4 type dry cells are used directly to supply LCD drive voltage, a lower limit voltage to guarantee an operation of the equipment is assumed to be 2V. The LCD is designed so as to provide fine display at V_{DD} = 2V, and M = 0. At that time, the apparent drive voltage is $V_{DD}' = V_{DD}$ = 2V since M = 0.

When the cell is sufficiently charged, that is, when V_{DD} = 3V, M value for making the apparent drive voltage to be V_{DD}' = 2V is calculated as:

$$M = 4 \times 3^2 / 2^2 - 4 = 5$$

and thereby, when the time period for causing the voltage difference between all common terminals and all segment terminals to be zero during T_0 = 5T is inserted, the apparent drive voltage may be held as V_{DD}' = 2V and fine display may be maintained.

When the control device is designed so as to measure the voltage by the voltage detector while using the equipment, to calculate the M value by the use of above expressions, and to drive the LCD based thereon, the fine LCD display may be provided within the range down to the lower limit voltage to guarantee the operation even if the cell discharges. This is effective

also on the weighing machine with body fat meter in which the visual angle varies widely depending on the using condition.

The liquid crystal material is arranged so that the same condition as that shown by the conventional example may be accomplished in which, when the LCD display being viewed from 40 degree of inclined direction, the effective value of voltage for reaching the brightness of N_{OFF} is 1.6 V and that for N_{ON} is 1.8 V, and, when the LCD display being viewed from 90 degree of inclined direction, the effective value of voltage for reaching the brightness of N_{OFF} is 2.4 V and that for N_{ON} is 2.8 V.

It is assumed that V_{DD} = 4.4 V, and, when the visual angle being 90 degree, it is assumed that M = 0, and, when the visual angle being 40 degree, it is assumed that M = 2.

For each case, unlighted outputs V_{4OFF} (0), V_{4ON} (0), V_{4OFF} (2), V_{4ON} (2) are calculated as follows:

V_{4OFF} (0) = 1.91 V $\quad$ V_{4ON} (0) = 2.91 V

V_{4OFF} (2) = 1.56 V $\quad$ V_{4ON} (2) = 2.38 V

When the visual angle is 40 degree, the brightness under the unlighted output V_{4OFF} (2) is higher than N_{OFF} and it is under unlighted condition. The brightness under the lighted output V_{4ON} (2) is sufficiently lower than N_{ON} and it is under lighted condition.

When the visual angle is 90 degree, the brightness under the unlighted output V_{4OFF} (0) is sufficiently higher than N_{OFF} and it is under unlighted condition. The brightness under the lighted output V_{4ON} (0) is lower than N_{ON} and it is under lighted

condition. Therefore, when it is 4 time division, the fine LCD display capable of coping with wide visual angle may be accomplished by setting as M = 0 or 2. This means that the fine LCD display may be accomplished while keeping the number of terminals of LCD and LCD drive unit to be small value. When the M value is increased, finer display with wider visual angle may be obtained.

In case where the invention is applied to the weighing machine with body fat meter, when the setting key is pushed, "setting mode" is employed and a person to be measured inputs his sex, year range, and height. Under "setting mode", the controller sets M = 2 for the counter for LCD drive. Thereby, under "setting mode", the finest display is provided for the visual angle of about 40 degree.

When the measuring key is pushed, "measuring mode" is employed in which a person to be measured takes standing posture and his weight and bio-impedance are measured. Under "measuring mode", the controller set M = 0 for the counter for LCD drive. Thereby, under "measuring mode", the finest display is provided for the visual angle of about 90 degree.

Though it is set to M = 2 in order to explain that wider visual angle is accomplished, M = 1 may be sufficient since the visual angle in an actual "setting mode" is near to 60 degree.

Though the output voltages of all common and segment terminals during T_0 time period are represented by square wave AC voltage V_Q for simple explanation, DC voltage, for example, V_G (= 1/2 V_{DD}), may be employed since the requirement is that

the output voltages of the common and the segment terminals are identical.

Though it is set to $T_0 = M * T$, unit time during T_0 time period may be "t" which is shorter than T. The shorter the "t" is, the more finely the length of T_0 time period may be set, and accordingly the apparent effective value of voltage, that is, the density of display, may be controlled more finely.

Though, in the present embodiment, the method for controlling the density of display by selecting either of "the state for always inserting the time period (T_0)" or "the state for always non-inserting the time period (T_0)" in 1 frame period T_F, another method may be employed in which N frame period is treated as 1 period and the state for the time period (T_0) to be inserted into only M frame periods among them is provided.

Since this allows the density of display which is between that in "the state for always inserting the time period (T_0)" and that in "the state for always non-inserting the time period (T_0)" to be formed artificially, the density of display can be adjusted more finely.

Though, in the embodiment, 4 time division is employed, it is a matter of course that better display quality may be accomplished when 3 time division or 2 time division is employed as a base.

In addition, since the basic principle is that the effective value of voltage is lowered relatively by inserting the time period for reducing the voltage applied to all segments to be zero, it is obvious that this principle is effective on not only

1/2 bias but also on 1/3 bias or 1/N bias.

Further, since it is not necessary that all common terminals and all segment terminals have the same voltage during T_0 time period but all the requirement is that the voltages applied to all terminal are lower, each of all common terminals and all segment terminals may have individual voltage respectively as long as the voltages applied to all segments are made lower within the range having non-negative effect on the display when the bias voltages is finely adjustable, for example, in case where N is large in 1/N bias.

As described above, the present invention provides the LCD drive unit which, in the equipment where the number of terminals of LCD is limited, may supply optimum density of display in response to the using condition without adjusting the drive voltage by volume knob or the like and with a little modification and with quite low cost.

In addition, since this unit allows to change the density of display with simple control, it can be easily controlled by the controller.

Though the density of the LCD display is usually turned paler to be indistinct when, in the equipment powered by the cell, the cell discharges and the LCD drive voltage lowers steeply, according to the present invention, it is easy to add the voltage detector and to attach the controller which adjusts the density of display automatically based on the detected data supplied from said voltage detector.

Further, in the weighing machine with body fat meter, since

a person to be measured looks at the LCD typically from inclined direction when he set his personal data, the density of display is in cross talk condition and is too dark to be read. Since he is in standing posture and looks at the LCD from approximately vertical direction when his weight and impedance value are measured, the display turns paler and falls into lack of contrast.

According to the present invention, the density of display is easily controlled so that the fine display density may be provided for the inclined angle direction when the personal data is set, and also the fine display density may be provided for the vertical direction during measuring.

As described above, the LCD drive unit of the present invention has noticeable effect that the fine LCD display may be provided without troublesome operation such as user's volume adjusting and with small numbers of terminals and quite low cost.

What is claimed is:

1. A LCD display device with display density adjusting function characterized by that, in an equipment which comprises a controller, a memory, a LCD drive unit of dynamic drive, a LCD, and an input device for distinguishing a using condition, a display density of said LCD having to be changed in response to the using condition thereof, said controller controls said LCD drive unit so as to be capable of inserting a time period (T_0) for outputting approximately same level of voltage to all common terminals and all segment terminals connected to the LCD into one frame period of LCD drive, and said controller selectively selects a value of time period (T_0) for outputting approximately same level of voltage to all common terminals and all segment terminals connected to said LCD based on information from said input device for distinguishing a using condition and adjusts a display density of the LCD.

ABSTRACT

A LCD display device with display density adjusting function characterized by that, in an equipment which comprises a controller, a memory, a LCD drive unit of dynamic drive, a LCD, and an input device for distinguishing a using condition, a display density of said LCD having to be changed in response to the using condition thereof, said controller controls said LCD drive unit so as to be capable of inserting a time period (T_0) for outputting approximately same level of voltage to all common terminals and all segment terminals connected to the LCD into one frame period of LCD drive, and said controller selectively selects a value of time period (T_0) for outputting approximately same level of voltage to all common terminals and all segment terminals connected to said LCD based on information from said input device for distinguishing a using condition and adjusts a display density of the LCD.

FIG. 1

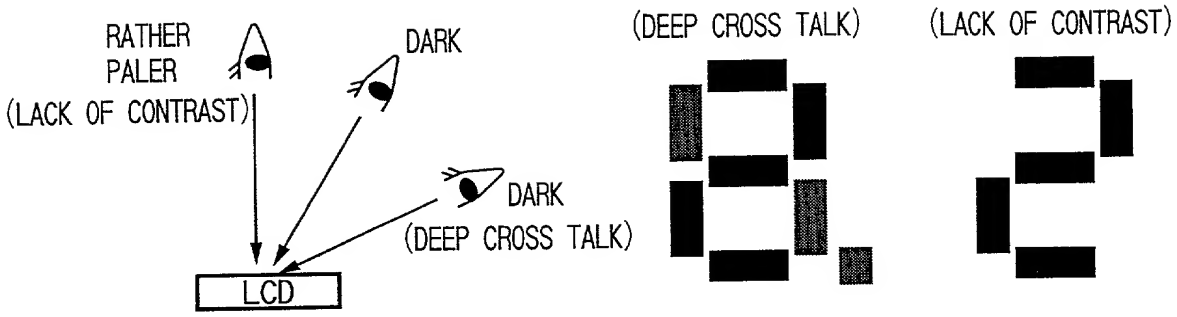

RATHER
PALER
(LACK OF CONTRAST)
DARK
DARK
(DEEP CROSS TALK)
LCD
(DEEP CROSS TALK)
(LACK OF CONTRAST)

FIG. 2

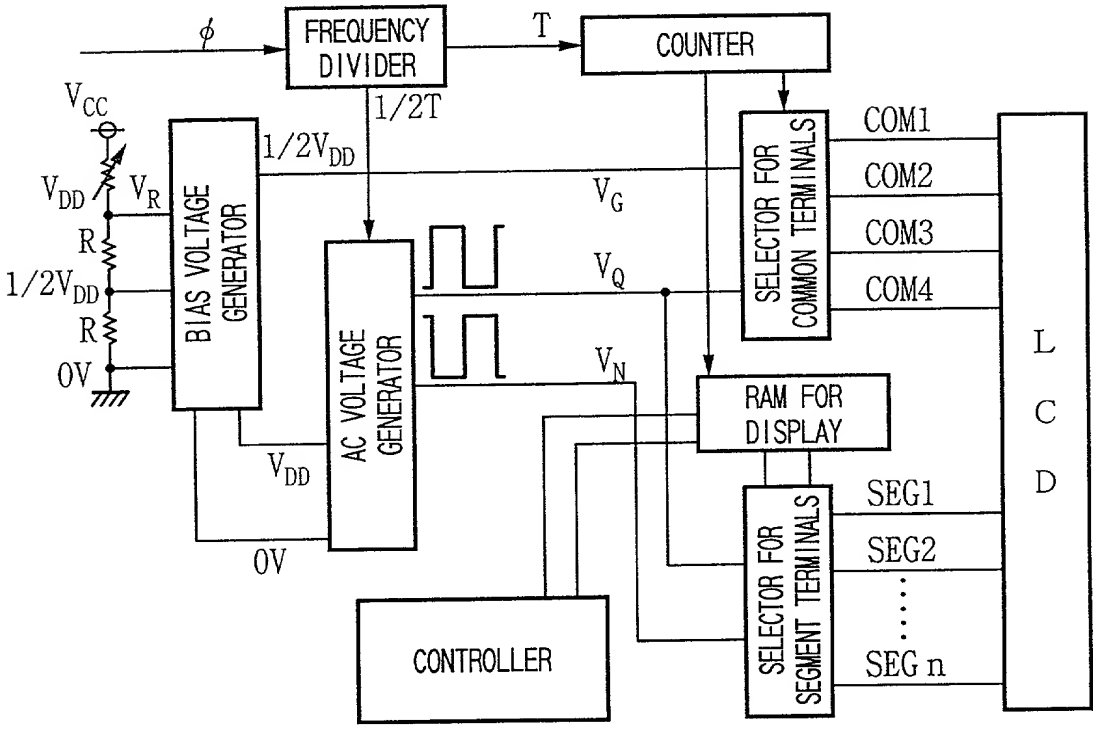

φ
FREQUENCY DIVIDER
T
COUNTER
1/2T
V_{CC}
V_{DD}
V_R
R
$1/2V_{DD}$
R
0V
BIAS VOLTAGE GENERATOR
$1/2V_{DD}$
V_G
V_Q
V_N
AC VOLTAGE GENERATOR
V_{DD}
0V
SELECTOR FOR COMMON TERMINALS
COM1
COM2
COM3
COM4
RAM FOR DISPLAY
SELECTOR FOR SEGMENT TERMINALS
SEG1
SEG2
SEG n
CONTROLLER
L
C
D

FIG. 3

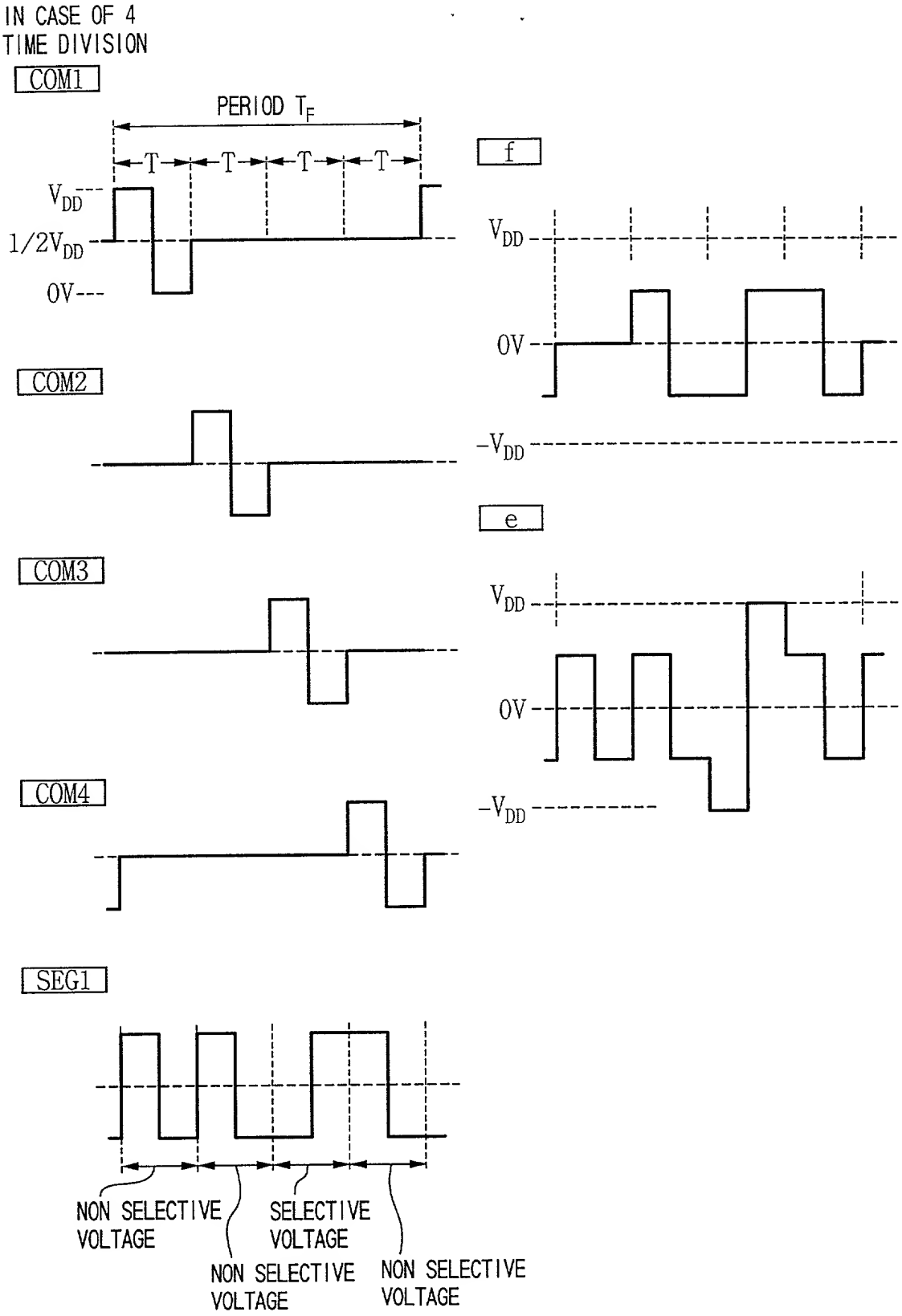

IN CASE OF 4
TIME DIVISION
COM1
PERIOD T_F
T
T
T
T
V_{DD}
$1/2V_{DD}$
0V
COM2
COM3
COM4
f
V_{DD}
0V
$-V_{DD}$
e
V_{DD}
0V
$-V_{DD}$
SEG1
NON SELECTIVE VOLTAGE
SELECTIVE VOLTAGE
NON SELECTIVE VOLTAGE
NON SELECTIVE VOLTAGE

FIG. 4

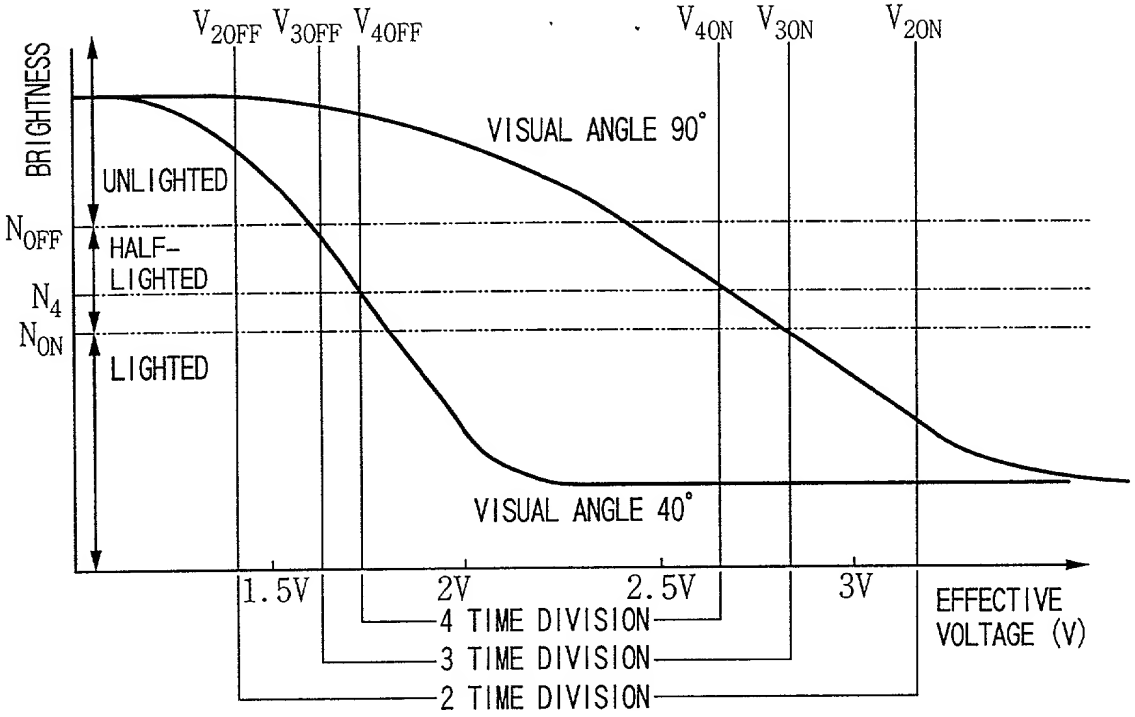

V_{20FF} V_{30FF} V_{40FF}
V_{40N} V_{30N} V_{20N}
BRIGHTNESS
VISUAL ANGLE 90°
UNLIGHTED
N_{OFF}
HALF-LIGHTED
N_4
N_{ON}
LIGHTED
VISUAL ANGLE 40°
1.5V
2V
2.5V
3V
EFFECTIVE VOLTAGE (V)
4 TIME DIVISION
3 TIME DIVISION
2 TIME DIVISION

FIG. 5

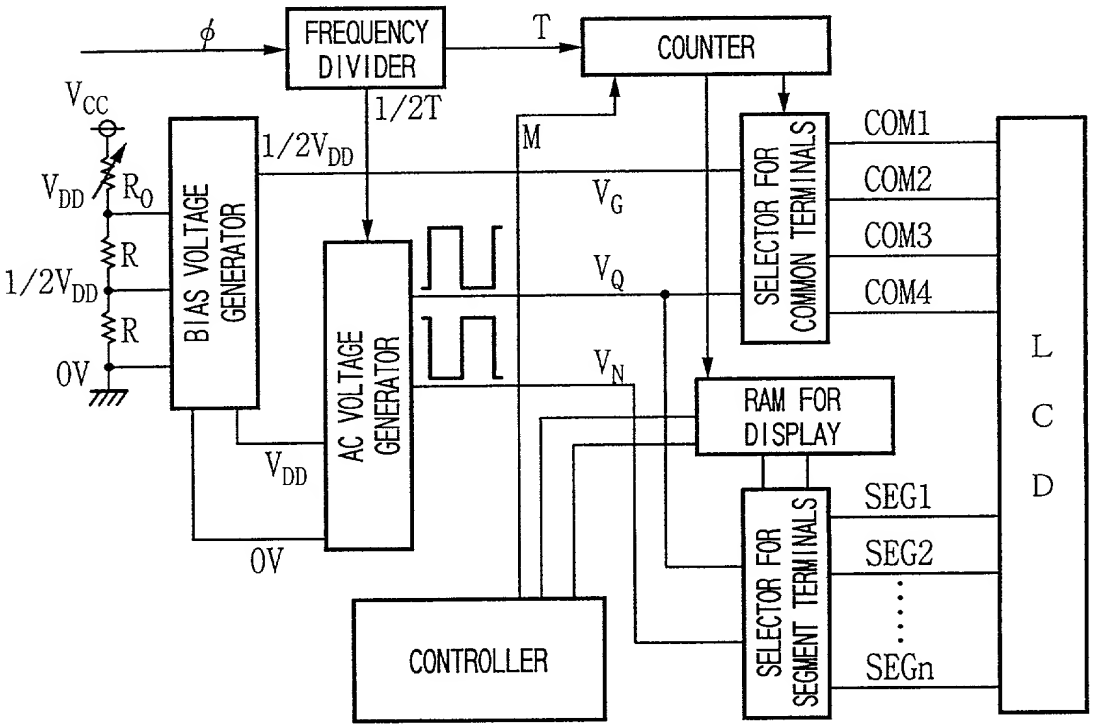

ϕ
FREQUENCY DIVIDER
T
COUNTER
1/2T
M
V_{CC}
V_{DD}
R_O
R
$1/2V_{DD}$
R
0V
BIAS VOLTAGE GENERATOR
$1/2V_{DD}$
V_G
V_Q
V_N
AC VOLTAGE GENERATOR
V_{DD}
0V
SELECTOR FOR COMMON TERMINALS
COM1
COM2
COM3
COM4
RAM FOR DISPLAY
SELECTOR FOR SEGMENT TERMINALS
SEG1
SEG2
SEGn
CONTROLLER
L C D

FIG. 6

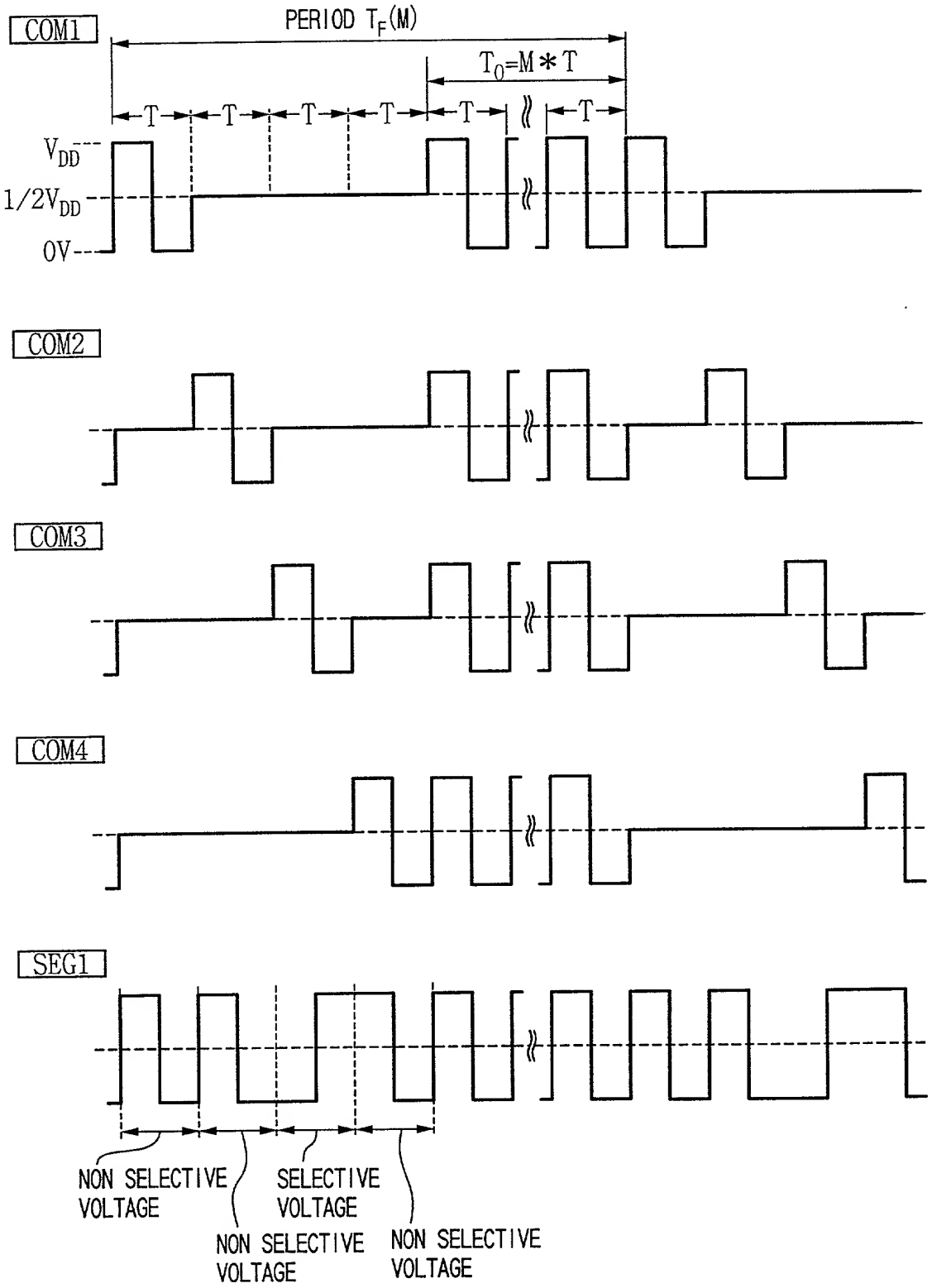

COM1
PERIOD $T_F(M)$
$T_0=M*T$
T
T
T
T
T
T
V_{DD}
$1/2V_{DD}$
0V
COM2
COM3
COM4
SEG1
NON SELECTIVE VOLTAGE
NON SELECTIVE VOLTAGE
SELECTIVE VOLTAGE
NON SELECTIVE VOLTAGE

FIG. 7

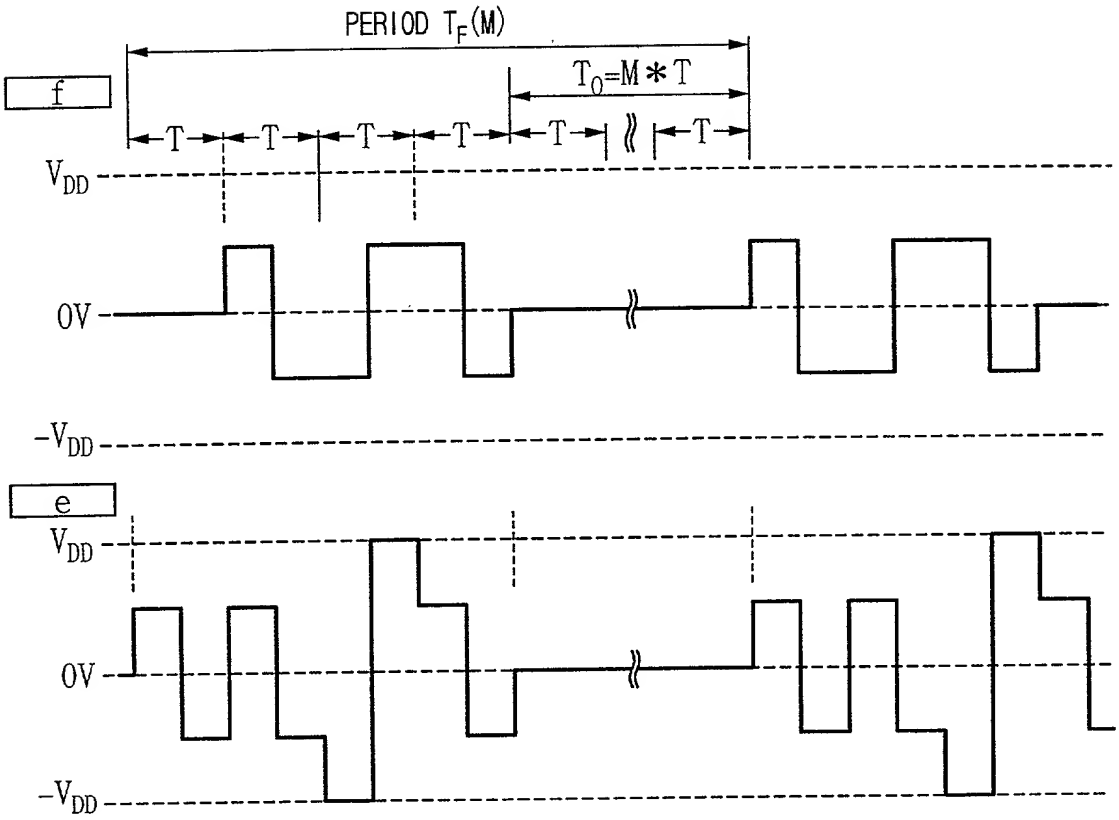
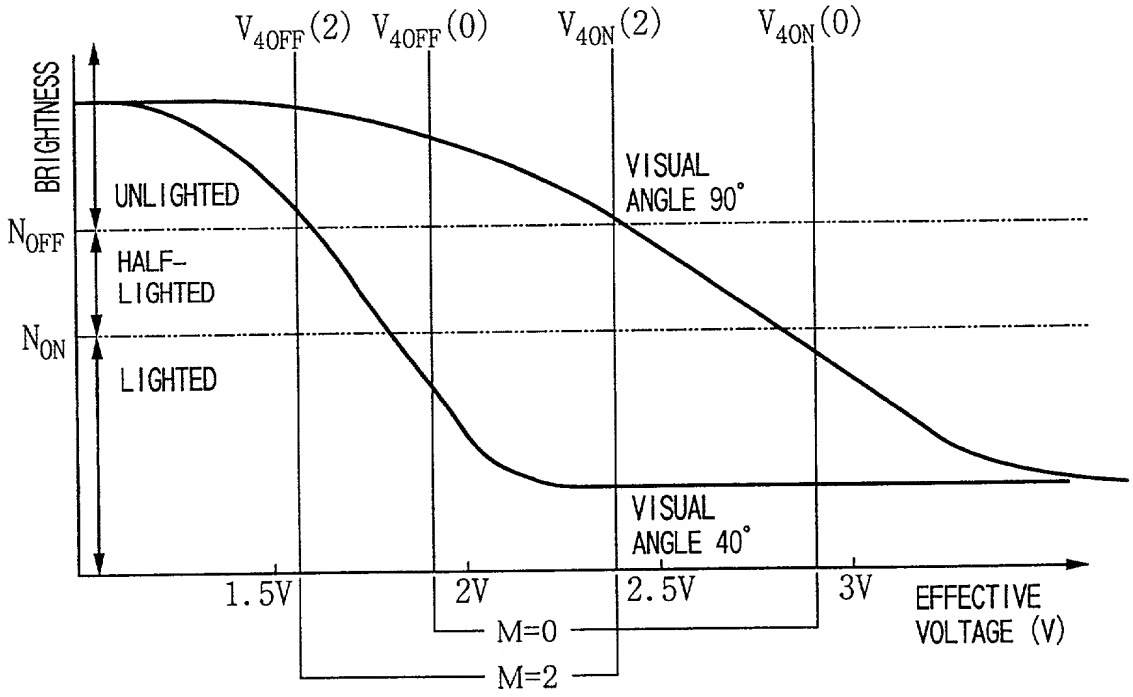

PERIOD $T_F(M)$
f
$T_0=M*T$
T
V_{DD}
0V
$-V_{DD}$
e
V_{DD}
0V
$-V_{DD}$

FIG. 8


$V_{4OFF}(2)$
$V_{4OFF}(0)$
$V_{4ON}(2)$
$V_{4ON}(0)$
BRIGHTNESS
UNLIGHTED
N_{OFF}
HALF-LIGHTED
N_{ON}
LIGHTED
VISUAL ANGLE 90°
VISUAL ANGLE 40°
1.5V
2V
2.5V
3V
EFFECTIVE VOLTAGE (V)
M=0
M=2

OP99013 -1 (US)

Attorney Docket No. _____

COMBINED DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor(s), I(we) hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled LCD DISPLAY DEVICE WITH DISPLAY DENSITY ADJUSTING FUNCTION, the specification of which

(check one)

XX is attached hereto.

___ was filed on _____ as United States Application No._____

___ PCT International Patent Application Number_____ filed _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
75098/1998 (Number)	JAPAN (Country)	10/03/98 (Day/Month/Year Filed)	XX Yes ___ No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	___ Yes ___ No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

PCT/JP99/01034 (Appln. Serial No.)	March 4,1999 (Filing Date)	Pending (Status-patented, pending, abandoned)
_____ (Appln. Serial No.)	_____ (Filing Date)	_____ (Status-patented, pending, abandoned)

And the said Assignor does hereby covenant and agree, for himself and his legal representatives, that he will assist the said Assignee in the prosecution of the application herein identified; in the making and prosecution of any other applications for Letters Patent that the said Assignee may elect to make covering the invention herein identified, as hereinabove set forth; in vesting in the said Assignee like exclusive title in and to all such other applications and Letters Patent; and in the prosecution of any interference which may arise involving said invention, or any application or Letters Patent herein contemplated; and that they will execute and deliver to the said Assignee any and all additional papers which may be requested by the said Assignee to fully carry out the terms of this Assignment.

And the Commissioner of Patents and Trademarks is hereby authorized and requested to issue Letters Patent to the said Assignee in accordance with the terms of this Assignment.

IN TESTIMONY WHEREOF, the said Assignor has hereunto set his hands and affixed his seal.

Date: October 13, 1999 — Koji Oguma
(Seal)
Koji OGUMA

WITNESSED:

Date: October 13, 1999 — Tsutomu Miyoshi
Tsutomu MIYOSHI

Date: October 13, 1999 — Hitoshi Sato
Hitoshi SATO

Date: _____ _____
(Seal)

WITNESSED:

Date: _____ _____

Date: _____ _____